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9-29-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
WATANABE *et al.*)
Application Number: To be assigned)
Filed: Concurrently herewith)
For: SIGNAL PROCESSING CIRCUIT)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Applicants have amended the claims in order to remove the multiple dependencies contained therein in accordance with standard U.S. practice, thereby reducing the basic filing fee. No new matter has been added to the application as a result of this amendment. Prior to an examination on the merits, please amend the above-identified application as follows:

IN THE CLAIMS:

Please substitute claims 3-10 currently on file with the following amended claims.

3. (Amended) A signal processing circuit according to Claim 1, further comprising a second input-output circuit that is connected to said system bus, produces an interrupt request to be issued to said microprocessor, and transfers data over said system bus under control of said microprocessor.
4. (Amended) A signal processing circuit according to Claim 1, further comprising a third input-output circuit that receives or transmits a serial signal, produces an interrupt request to be issued to said microprocessor, and transfers the serial signal over said system bus under control of said microprocessor.

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